

**Notice of References Cited**

Application/Control No.

09/736,567

Applicant(s)/Patent Under  
Reexamination  
BRENNER ET AL.

Examiner

Clifford H Knoll

Art Unit

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-2002/0099933	07-2002	Nevill et al.	712/228
	C	US-			
	D	US-			
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	K	US-			
	L	US-			
	M	US-			

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Disabling Interrupts at Processor Level, Atmel AT91 ARM THUMB Microcontrollers, Application Note, Rev 1156A, 8/98, 3 pp. ( <a href="http://www.eetasia.com/ARTICLES/2000AUG/2000AUG29_CT_AN7.PDF">http://www.eetasia.com/ARTICLES/2000AUG/2000AUG29_CT_AN7.PDF</a> )
	V	Application Note 25: Exception handling on the ARM (including thumb-aware processors), ARM DAI 0025E, Advanced RISC Machines Ltd (ARM) September 1996, 36 pp.
	W	Application Note 31: Using embeddedICE, ARM DAI 0031C, Advanced RISC Machines Ltd (ARM) February 1999, 30 pp.
*	X	ARM architecture reference manual, ver B (et seq.), 26 July 1996, pp. 2.1-2.10.

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

